AMENDMENT UNDER 37 C.F.R. 1.116 – EXPEDITED PROCEDURE

Serial Number: 10/812,277

Filing Date: March 29, 2004

Title: SEMICONDUCTING DEVICE WITH FOLDED INTERPOSER

Assignee: Intel Corporation

THE CLAIMS

Page 4

Dkt: 884.C26US1 (INTEL)

Please amend the claims as follows:

1. (Original) A semiconducting device comprising:

an interposer that includes a fold which divides the interposer into a first section and a

second section;

a first die attached to a first surface of the interposer at the first section and the second

section;

a contact attached to the first surface of the interposer at the first section and the second

section;

a second die attached to a second surface of the interposer, the second die being stacked

onto the first die and electrically coupled to the first die by the contact and conductive paths that

are part of the interposer.

2. (Original) The semiconducting device of claim 1, wherein the contact is a solder column.

Claims 3-4. (Cancelled)

5. (Original) The semiconducting device of claim 1, further comprising a plurality of

contacts that are each attached to the first surface of the interposer at the first section and the

second section.

6. (Original) The semiconducting device of claim 5, wherein the fold is on one side of the

first die and at least one of the contacts is on an opposite side of the first die.

7. (Original) The semiconducting device of claim 5, wherein at least one of the contacts is

on each side of the first die.

Serial Number: 10/812,277 Filing Date: March 29, 2004

Title: SEMICONDUCTING DEVICE WITH FOLDED INTERPOSER

Assignee: Intel Corporation

Claims 8-20. (Cancelled)

(Currently Amended) The semiconducting device of claim 1, wherein the contact is 21.

Page 5

Dkt: 884.C26US1 (INTEL)

formed of a solder column and a pad.

(Previously Presented) The semiconducting device of claim 1, wherein the contact 22.

extends between two different portions of the first surface.

(Previously Presented) The semiconducting device of claim 1, wherein at least one of the 23.

contacts is on one side of the first die and at least one other of the contacts is on an opposing side

of the first die.

(Previously Presented) The semiconducting device of claim 1, wherein at least one of the 24.

contacts is on three sides of the first die.

25. (Previously Presented) The semiconducting device of claim 1, wherein at least one of the

contacts is on each side of the first die.

(Previously Presented) A semiconducting device comprising: 26.

an interposer that includes a fold which divides the interposer into a first section and a

second section;

a first die attached to a first surface of the interposer at the first section and the second

section;

a solder column attached to the first surface of the interposer at the first section and the

second section;

a second die attached to a second surface of the interposer, the second die being stacked

onto the first die and electrically coupled to the first die by the solder column and conductive

paths that are part of the interposer.

Page 6
Dkt: 884.C26US1 (INTEL)

## AMENDMENT UNDER 37 C.F.R. 1.116 - EXPEDITED PROCEDURE

Serial Number: 10/812,277 Filing Date: March 29, 2004

Title: SEMICONDUCTING DEVICE WITH FOLDED INTERPOSER

Assignee: Intel Corporation

27. (Previously Presented) The semiconducting device of claim 26, further comprising a plurality of solder columns that are each attached to the first surface of the interposer at the first section and the second section.

- 28. (Previously Presented) The semiconducting device of claim 27, wherein at least one of the solder columns is on each side of the first die.
- 29. (Previously Presented) A semiconducting device comprising:

an interposer that includes a fold which divides the interposer into a first section and a second section;

a first die attached to a first surface of the interposer at the first section and the second section;

a plurality of contacts attached to the first surface of the interposer at the first section and the second section, the fold being on one side of the first die and at least one of the contacts being on an opposite side of the first die;

a second die attached to a second surface of the interposer, the second die being stacked onto the first die and electrically coupled to the first die by the plurality of contacts and conductive paths that are part of the interposer.

- 30. (Previously Presented) The semiconducting device of claim 29, wherein each of plurality of contacts is a solder column.
- 31. (Previously Presented) The semiconducting device of claim 30, wherein at least one of the contacts is on each side of the first die.